

INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>				Docket Number (Optional) YOR919980324 S2	Application Number Not Yet Assigned		
				Applicant(s) Stephen M. GATES et al.			
				Filing Date Concurrently Herewith	Group Art Unit Not Yet Assigned		
U.S. PATENT DOCUMENTS							
*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
P		5,640,343	06/1997	Gallagher et al.			
P		5,734,605	03/1998	Zhu et al.			
P		6,097,625	08/2000	Scheuerlein			
P		6,180,444	01/2001	Gates et al.			
P		6,190,998-B1	02/2001	Bruel et al.			
P		6,242,770	06/2001	Bronner et al.			
FOREIGN PATENT DOCUMENTS							
REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO
OTHER DOCUMENTS <i>(Including Author, Title, Date, Pertinent Pages, Etc.)</i>							
P		M. Bruel "The History, Physics, and Applications of the Smart-Cut ® Process MRS Bulletin, Dec. 1998, pp.35-39					
P		B. Aspar et al., (Elsevier Science B.V.) MicroElectronic Engineering 36 (1997) 233-240 "Basic mechanisms involved in the Smart-Cut ® process"					
EXAMINER PHAT X. CAO				DATE CONSIDERED 8/30/02			

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W. J. Gallagher et al., J. Appl. Phys. 81 (8) 15 April 1997, "Microstructured magnetic tunnel junctions (invited)" pp. 3741-3746.

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R. DeJule, "Bringing SDI Wafer Technology to the Mainstream", SEMICONDUCTOR INTERNATIONAL, 11/1998 P40

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J. Markoff "A Milestone on the Road to Ultrafast Computers" New York Times April 6, 1999

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